ket No.: 2000P4162

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Date: April 9, 2002

Inventor

Knut Kahlisch et al.

Applic. No. Filed

09/901,550 July 9, 2001

Title

Examiner

Support Matrix with Bonding Channel for Integrated

Semiconductors, and Method for Producing it Chris C. Chu - Art Unit: 2815

RESPONSE

Hon, Commissioner of Patents and Trademarks, Washington, D. C. 20231

Sir:

Responsive to the Office Action dated March 15, 2002, the following remarks are made:

In deference to the restriction requirement on pages 2 and 3 of the above-identified Office Action, applicants elect group I, claims 1-7 for prosecution at this time.

Applicants reserve the right to file a divisional application including the method claims of group II at a later date and/or to request a rejoinder under MPEP §821.04.

In view of the foregoing, the early issuance of an Action on the merits of claims is solicited.

Respectfully submitted,

For Applicants

MARKUS NOLFF REG. NO. 37,006

Date: April 9, 2002

Lerner and Greenberg Post Office Box 2480 Hollywood, FL 33022-2480 Tel: (954) 925-1100

Fax: (954) 925-1101 /kf